



EXPRESS MAIL MAILING LABEL NO. EV 218962438 US

PATENT APPLICATION
Docket No: 15436.253.31

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	Andreas Weber	
Serial No.:	09/929,737	
Filing Date:	August 31, 2001	Art Unit 2633
Confirmation No.:	9021	
For:	MULTI-RATE AND MULTI-LEVEL GIGABIT INTERFACE CONVERTER	

REVOCATION AND SUBSTITUTE
POWER OF ATTORNEY AND
STATEMENT UNDER 37 CFR 3.73(b)

Honorable Commissioner of Patents
and Trademarks
Washington, DC 20231

Sir:

I, Frank H. Levinson, state that I am Chairman of the Board of Finisar Corporation and that I am authorized to execute this Revocation and Substitute Power of Attorney on behalf of Finisar Corporation.

I further state that Finisar Corporation is the assignee of the entire interest of the above-identified patent or patent application as shown by the assignment(s) recorded in the U.S. Patent and Trademark Office at the Reel and Frame identified in Exhibit A; The assignee, Finisar Corporation, hereby revokes all previous powers of attorney in the above-identified application, which is included in the schedule of U.S. Patents and Patent Applications of Exhibit B, and now hereby appoints all attorneys under customer number:



022913

PATENT TRADEMARK OFFICE

of WORKMAN, NYDEGGER & SEELEY, 1000 Eagle Gate Tower, 60 East South Temple, Salt Lake City, Utah 84111, as attorneys with full power of substitution and revocation, to prosecute said application, to make alterations and amendments therein, to receive the Letters Patent, and to transact all business in the Patent and Trademark Office connected therewith.

All correspondence and telephonic communication should be directed to:

ERIC L. MASCHOFF
WORKMAN, NYDEGGER & SEELEY
1000 Eagle Gate Tower
60 East South Temple
Salt Lake City, Utah 84111

This Revocation and Substitute Power of Attorney and Statement Under 37 CFR 3.73(b) is effective for all of the U.S. Patents and Patent Applications of Exhibit B, and shall be filed at the U.S. Patent & Trademark Office in all of said U.S. Patents and Patent Applications.

Signed this 28 day of May, 2003.

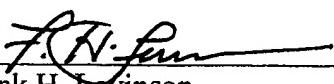

Frank H. Levinson
Finisar Corporation
1308 Moffet Park Drive
Sunnyvale, California 94089

EXHIBIT A

An assignment from the inventor(s) of U.S. Patent Application Serial No. 09/929,737
filed August 13, 2001 has been recorded in the U.S. Patent and Trademark Office at
Reel 012082, Frame 0294.

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EXHIBIT B Patents and Patent Applications Subject to Revocations and Substitute Power of Attorney

WILCOX NUMBER	PATENT NUMBER	ISSUE DATE	FILING DATE	ISSUE DATE		
15436.253.1	9775-005-999	HIGH SPEED MODIFICATION SYSTEM AND METHOD	09/327,987	8-Jun-99	6268808	31-Jul-01
15436.253.2	9775-007-999	MULTI-PROTOCOL DUAL FIBER LINK LASER DIODE CONTROLLER AND METHOD	08/924,852	5-Sep-97	5856168	21-Sep-99
15436.253.7	9775-015-999	HIGH SPEED NETWORK SWITCH INTEGRATED OPTICAL COUPLER AND CONNECTOR	08/440,088	12-May-95	5604735	18-Feb-97
15436.253.8	9775-016-999		07/189,978	26-May-88	4881789	21-Nov-89
15436.253.9	9775-017-999	2X2 OPTICAL BYPASS SWITCH	07/358,892	30-May-89	4927225	22-May-90
15436.253.12	9775-020-999	HIGH SPEED INFORMATION BROADCASTING SYSTEM	07/778,453	1-Nov-91	5404505	4-Apr-95
15436.253.13	9775-021-999	SEMICONDUCTOR LAS DIODE CONTROLLER AND LASER DIODE BIASING CONTROL METHOD	07/583,178	14-Sep-90	5019769	28-May-91
15436.253.14	9775-022-999	HIGH SPEED MESH CONNECTED LOCAL AREA NETWORK	08/404,873	15-Mar-95	5566171	15-Oct-96
15436.253.16	9775-026-999	A PRECISION GaAs LOW-VOLTAGE DC AMPLIFIER	09/221,673	23-Dec-98	6121838	19-Sep-00
15436.253.17	9775-030-999	A TRANSCEIVER WITH AUXILIARY MONITORING PORTS	09/420,947	19-Oct-99		
15436.253.18	9775-031-999	FIBER OPTIC LASER TRANSMITTER WITH REDUCED NEAR END REFLECTIONS	09/521,639	8-Mar-00		
15436.253.18.1	9775-034-999	SIGNAL STRENGTH DETECTION IN HIGH SPEED OPTICAL ELECTRONICS	10/285,083	31-Oct-02		
15436.253.21	9775-036-999	SIGNAL STRENGTH DETECTION IN HIGH-SPEED OPTICAL ELECTRONICS	10/285,106	31-Oct-02		
15436.253.23	9775-039-999	COMPACT OPTICAL ASSEMBLY FOR OPTOELECTRONIC TRANSCEIVERS	09/957,557	19-Sep-01		
15436.24.1	9775-040-999	SYSTEM AND METHOD FOR TRANSMITTING DATA ON RETURN PATH OF A CABLE TELEVISION SYSTEM	09/735,710	12-Dec-00		
15436.253.25.1	9775-042-999	A SYNCHRONOUS NETWORK TRAFFIC PROCESSOR	09/978,765	12-Oct-01		
15436.253.28.1	9775-043-999	OPTOELECTRONIC DEVICE CAPABLE OF PARTICIPATING IN IN-BAND TRAFFIC	10/003,859	14-Nov-01		
15436.253.28	9775-047-999	FIBER OPTIC HEADSET FOR WIRELESS TELEPHONES	08/691,311	17-Oct-00		
15436.253.29	9775-048-999	CIRCUIT INTERCONNECT FOR OPTOELECTRONIC DEVICE FOR CONTROLLED IMPEDANCE AT HIGH FREQUENCIES	10/005,924	4-Dec-01		
15436.253.31	9775-051-999	MULTI-RATE AND MULTI-LEVEL GIGABIT INTERFACE CONVERTER	09/929,737	31-Aug-01		
15436.253.32	9775-052-999	INTEGRATED MEMORY MAPPED CONTROLLER CIRCUIT FOR FIBER OPTICS TRANSCEIVERS	09/777,917	5-Feb-01		
15436.253.33	9775-055-999	SIGNAL PROCESSING CIRCUIT FOR FLOATING SIGNAL SOURCES USING POSITIVE FEEDBACK	08/926,176	8-Aug-01		
15436.253.35.1	9775-057-999	SYSTEM AND METHOD FOR PACKAGING A LASERDETECTOR	09/823,471	6-Aug-01		
15436.253.37.1	9775-059-999	OPTOELECTRONIC TRANSCEIVER MODULE WITH THERMALLY ISOLATED COMPONENTS	10/101,247	18-Mar-02		
15436.253.39.1	9775-063-999	COMPACT LASER PACKAGE WITH INTEGRATED TEMPERATURE CONTROL	10/101,260	18-Mar-02		

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EXHIBIT B Patents and Patent Applications Subject to Revocations and Substitute Power of Attorney

INVENTOR	PATENT NUMBER	NAME OF INVENTION	Serial No.	Filing Date	Patent No.	Issue Date
15436.253.40.1	9775-065-999	CONTROL CIRCUIT FOR OPTOELECTRONIC MODULE WITH INTEGRATED TEMPERATURE CONTROL	10/101,248	18-Mar-02		
15436.253.41.1	9775-070-999	BANDPASS COMPONENT DECIMATION AND TRANSMISSION OF DATA IN CABLE TELEVISION DIGITAL RETURN PATH	10/218,344	12-Aug-02		
15436.253.42.1	9775-071-999	DATA RATE COMPRESSION DEVICE FOR CABLE TELEVISION RETURN PATH USING BANDPASS PUNCTURING	10/102,619	3/19/2002		
15436.253.43.1	9775-072-999	APPARATUS AND METHOD FOR COMBINING ASYNCHRONOUS DIGITAL SIGNALS IN CABLE TELEVISION RETURN PATH	10/357,918	3-Feb-03		
15436.253.44.1	9775-073-999	AVALANCHE PHOTODIODE CONTROLLER CIRCUIT FOR FIBER OPTICS TRANSCEIVER	10/101,258	18-Mar-02		
15436.253.45	9775-074-999	MULTIPLE WIDTH TRANSCEIVER HOST BOARD SYSTEM	10/036,995	22-Oct-01		
15436.253.46.1	9775-075-999	CABLE TELEVISION RETURN LINK SYSTEM WITH DATA-RATE SIDE-BAND COMMUNICATION CHANNELS	10/285,205	30-Oct-02		
15436.253.47.1	9775-076-999	EFFICIENT TRANSMISSION OF DIGITAL RETURN PATH DATA IN CABLE TELEVISION RETURN PATH	10/102,625	19-Mar-02		
15436.253.48	9775-085-999	SYSTEM FOR CONTROLLING BIAS CURRENT IN LASER DIODES WITH IMPROVED SWITCHING RATES	10/188,575	2-Jul-02		
15436.253.49.1	9775-086-999	TRANSMITTER OPTICAL SUBASSEMBLY WITH VOLUME PHASE HOLOGRAPHIC OPTICS	10/351,620	23-Jan-03		
15436.253.50.1	9775-087-999	EXTENDED BANDWIDTH SEMICONDUCTOR OPTICAL AMPLIFIERS	10/348,341	21-Jan-03		
15436.253.51.1	9775-088-999	METHOD FOR MAINTAINING DESIRABLE OPTICAL PERFORMANCE OF LASER EMITTERS OVER TEMPERATURE VARIATIONS	10/285,105	31-Oct-02		
15436.253.52.1	9775-090-999	TRANSISTORS OUTLINE PACKAGE WITH EXTERIORLY MOUNTED RESISTERS	10/393,215	19-Mar-03		
15436.253.53.1	9775-091-999	A SUBMOUNT, PEDESTAL, AND WIRE BOND ASSEMBLY FOR A TRANSISTOR OUTLINE PACKAGE WITH REDUCED WIRE BOND INDUCTANCE	10/393,218	19-Mar-03		
15436.253.54.1	9775-092-999	TRANSMISSION IMPEDANCE AMPLIFIER ASSEMBLY WITH SEPARATE GROUND LEADS AND SEPARATE POWER LEADS FOR INCLUDED CIRCUITS	10/285,204	30-Oct-02		
15436.253.55.1	9775-093-999	A TRANSMISSION LINE WITH INTEGRATED CONNECTION PADS	10/383,164	19-Mar-03		
15436.253.56.1	9775-094-999	CIRCUIT BOARD HAVING TRACES WITH DISTINCT TRANSMISSION IMPEEDANCES	10/393,217	19-Mar-03		
15436.253.57	9775-095-999	A SYSTEM AND METHOD OF PROCESSING DATA SIGNAL	10/285,082	31-Oct-02		
15436.253.58.1	9775-096-999	A SYSTEM AND METHOD OF DETECTING A BIT PROCESSING ERROR	10/285,081	31-Oct-02		

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EXHIBIT B Patents and Patent Applications Subject to Revocations and Substitutions Power of Attorney

INVENTOR(S)	PATENT LAW	NAME OF INVENTION	DATE FILED	DATE ISSUED	STAT	FILING DATE	PATENT #	ISSUE DATE
15436.253.59.1	9775-098-998	APPARATUS FOR ENHANCING IMPEDANCE-MATCHING IN A HIGH-SPEED DATA COMMUNICATIONS SYSTEM	10/285,772	1-Nov-02				
15436.253.60	9775-101-998	APPARATUS AND METHOD FOR REDUCING INTERFERENCE IN AN OPTICAL DATA STREAM	10/288,324	5-Nov-02				
15436.253.83	9775-103-998	SYSTEM AND METHOD FOR PROTECTING EYE SAFETY DURING OPERATION OF A FIBER OPTIC TRANSCEIVER	10/268,869	8-Oct-02				
15436.253.62	9775-105-998	OPTICAL TRANSCIEVER MODULE WITH A SINGLE INTERNAL SERIAL BUS	10/268,870	8-Oct-02				
15436.253.63	9775-107-998	SYSTEM AND METHOD FOR TESTING A LASER MODULE BY MEASURING ITS SIDE MODE SUPPRESSION RATIO	10/246,038	18-Sep-02				
15436.253.64	9775-109-998	SIGNAL PROCESSING CIRCUIT FOR FLOATING SIGNAL SOURCES USING POSITIVE FEEDBACK	10/147,677	16-May-02				
15436.253.69.1	9775-127-998	DUAL FIBER OPTIC AMPLIFIER WITH SHARED PUMP SOURCE	10/384,228	7-Mar-03				
15436.253.70.1	9775-128-998	STAGED AMPLIFIER FOR LOWER NOISE FIGURE AND HIGHER SATURATION POWER	10/384,227	7-Mar-03				
15436.253.72.1	9775-130-998	SYSTEM FOR CONTROLLING BIAS CURRENT IN LASER DIODES WITH IMPROVED SWITCHING RATES	10/285,203	30-Oct-02				
15436.253.76	9775-137-998	MAINTAINING DESIRABLE PERFORMANCE OF OPTICAL EMITTERS AT EXTREME TEMPERATURES	10/285,369	31-Oct-02				
15436.253.79	9775-145-998	EFFICIENT TRANSMISSION OF DIGITAL RETURN PATH DATA IN CABLE TELEVISION RETURN PATH	10/231,208	8-Nov-02				
15436.253.81	9775-152-998	METHOD AND APPARATUS FOR REDUCING INTERFERENCE IN AN OPTICAL DATA STREAM USING DATA-INDEPENDENT EQUALIZATION	10/419,023	17-Apr-03				

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For:	MULTI-RATE AND MULTI-LEVEL GIGABIT INTERFACE CONVERTER	
Examiner:	Mary Benton	

CHANGE OF ATTORNEY DOCKET NUMBER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

For convenience and ready identification of the papers received in connection with the above-identified patent application, please reference in all future communications my Docket No. 15436.253.31. All communications should be addressed to:

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Dated this 9 day of June, 2003.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

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